

## BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re Application of : Jong Shik YOON et al. Confirmation No.: 8658  
Application Number : 10/825,343  
Filed : April 15, 2004  
Title : MINIMIZING TRANSISTOR VARIATIONS DUE TO SHALLOW  
TRENCH ISOLATION STRESS  
TC/Art Unit : 2893  
Examiner: : Thanh T. Nguyen  
  
Docket No. : TI-37043 (0025.0245)  
Customer No. : 23494

**Mail Stop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**NOTICE OF APPEAL**

Sir:

Applicants hereby appeal to the Board of Appeals from the decision dated July 22, 2008 of the Examiner.

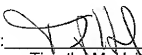
The item(s) checked below are appropriate:

1. ☐ A Petition for extension of time to respond to the final rejection was filed on [Date] extending the period for response to [Date].
2. ☒ A Petition for extension of time to further extend the period for response to January 22, 2009 with a fee of \$1,110.00 is to be charged to Deposit Account No. 20-0668.
3. ☒ The Appeal Fee of \$540.00 (or \$270.00 if by small entity)  
☐ is enclosed or ☒ is to be charged to Deposit Account No. 20-0668
4. ☐ A check for \$[Fee] to cover the above fees is enclosed.

Please grant any extensions of time required to enter this response and charge any additional required fees to Texas Instruments' deposit account 20-0668.

Respectfully submitted,

Dated: January 22, 2009

By:   
Timothy M. Hsieh  
Reg. No. 42,672

MH2 Technology Law Group, LLP